## INFORMATION DISCLOSURE STATEMENT

Atty Docket: Serial No.: Applicant: Filing Date: Group: 00ED18852609 Not Yet Assigned RAYNOR ET AL. Herewith 2015

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**U.S. PATENT DOCUMENTS** Examiner **Document Date** Name Class Sub **Filing Date** Number Initials Class AA 5,055,930 10/8/91 Nagasaki et al. 358 213.11 AB AC ΑD ΑE AF AG AH ΑI AJ ΑK ΑL **FOREIGN PATENT DOCUMENTS Document** Date Country Class Sub **Translation** Number Class AM AN OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.) Itoh et al. "4-Layer 3-D IC with a Function of Parallel Signal Processing" AQ aus Microelectronic Engineering, NL, Elsevier Publishers BV., Amsterdam, vol. 15, no. 1/04, 10/1/1991, pages 187-190, XP000292757 Akasaka et al. "The 3-D IC with 4-Layer Structure for the Fast Range Sensing AR ar System" Microelectronic Engineering, NL, Elsevier Publishers BV., Amsterdam, vol. 15, no. 1/04, 10/1/1991, pages 183-186, XP000292756 Zhou et al. "Frame-Transfer CMOS Active Pixel Sensor with Pixel Binning" IEEE AS Transactions on Electron Devices, US, IEEE Inc. New York, vol. 44, no. 10,

EXAMINER: DATE CONSIDERED: 6/7/08
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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